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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/726,945

12/02/2003

Miguel A. Perez

5348

25859

7590

10/21/2005

WEI TE CHUNG  
FOXCONN INTERNATIONAL, INC.  
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EXAMINER

LEE, CHUN KUAN

ART UNIT

PAPER NUMBER

2181

DATE MAILED: 10/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/726,945

Applicant(s)

PEREZ ET AL.

Examiner

Chun-Kuan (Mike) Lee

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 02 December 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 02 December 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States

1. Claims 1-5 and 7-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Richter et al. (US Patent 5,727,184).

2. As per claim 1, Richter teaches a computer interconnecting system method comprising:

a system adapter interface card comprising a first interface compatible with ATA standard and PCMCIA standard (Abstract and Ref. # 202 in Figure 2A, where "system adapter interface card" is read on "circuit board" and "ATA standard and PCMCIA standard" is read on "at least two signal transmission standards");

a PCMCIA Card having a second interface conforming to PCMCIA standard (Ref. # 122, 123, 222 and 223 in Figure 2A, where "PCMCIA Card" is read on "electronic component" and "PCMCIA standard" is read on "one of the at least two standards"); and

a system adapter (Ref. # 204 in Figure 2A, where "system adapter" is read on "translation adapter") interconnecting the circuit board and the PCMCIA Card, wherein the system adapter having a board-mating interface interfacing with the first interface and a component-mating interface interfacing with the second interface (Figure 2A).

3. As per claims 2, Richter teaches the computer interconnecting system method comprising wherein the system adapter interface card comprise of chips and integrated

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circuit arrange thereon (Ref. # 204 in Figure 2A and column 4, lines 60-66, where "chips and integrated circuit " is read on "a plurality of electronic elements").

4. As per claim 3, Richter teaches the computer interconnecting system method comprising wherein the system adaptor interface card defines a mating side and a single ribbon cable is coupled along the mating side (Abstract and Ref. # 132 in Figure 2A, where "single ribbon cable" is read on "plurality of Input/Output connectors").

5. As per claim 4, Richter teaches the computer interconnecting system method comprising wherein the PCMCIA card which is an electrical card (Ref. # 122, 123, 222 and 223 in Figure 2A).

6. As per claim 5, Richter teaches the computer interconnecting system method comprising wherein the system adapter comprise a first connector having the board-mating interface, a second connector having the component-mating interface (Ref. # 132, 134, 135 and 237 in Figure 2A) and a board interconnecting the first and second connectors (Ref. # 202 in Figure 2A).

7. As per claim 7, Richter teaches the computer interconnecting system method comprising wherein the system adapter interface card has an electrical connector having the first interface therewith (Ref. # 132 in Figure 2A).

8. As per claim 8, Richter teaches the computer interconnecting system method comprising:

a circuit board defining a base interface thereof (Ref. # 100 in Figure 1B);  
a first and a second electronic component different from each other and respectively defining different first and second interfaces thereof (Ref. # 114, 115, 122, 123, 134, 135 and 172 in Figure 1B);  
first and second adapters different from each other, the first adapter defining a board-mating interface adapted to be mated with the base interface and a first component-mating interface opposite to said board-mating interface and adapted to be mated with the first interface (Ref. # 110, 132, 134 and 135 in Figure 1B) and the second adapter defining another board-mating interface similar to said board-mating interface and a second component-mating interface opposite to said another board-mating interface and adapted to be mated with the second interface (Ref. # 133, 170 and 172 in Figure 1B), said first component-mating interface being different from said second component-mating interface (Figure 1B); and  
wherein said first component along with the first adapter and said second component along with the second adapter are mutually exclusively assembled with the circuit board (Figure 1B).

***Claim Rejections - 35 USC § 103***

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

9. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Richter et al. (US Patent 5,727,184) in view of the "Diamond Monster Sound MX300 Review".

10. As per claim 6, Richter as modified teaches that the system adapter interface card may be a single interface expansion board (Abstract).

Richter fails to teach that the interface expansion board has plurality of golden fingers forming the first interface.

The "Diamond Monster Sound MX300 Review" teaches that the expansion board has a plurality of golden-plated connectors forming the interface (MX300 soundcard Figure and 1<sup>st</sup> paragraph).

It would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified Richter to implement the interface expansion board with the plurality of golden fingers forming the first interface.

It would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified Richter by the teaching of The "Diamond Monster

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Sound MX300 Review” because it is well knowledge in the art that gold plated connector conduct signals better.

**Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chun-Kuan (Mike) Lee whose telephone number is (571) 272-0671 and email is chun-kuan.lee@uspto.gov. The examiner can normally be reached on 8AM to 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Popovici Dov can be reached on (571)272-4083. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300. Any inquiry of a general nature of relating to the status of this application should be directed to the Group receptionist whose telephone number is (571) 272-2100.

Mailed responses to this action should be sent to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231.

Faxes for Official/formal (After Final) communications or for informal or draft communications (please label "PROPOSED" or "DRAFT") sent to:

(571) 273-8300


Hand-delivered responses should be brought to:

USTPO, Randolph Building, Customer Service Window

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Alexandria, VA 22314

C.K.L.  
10/03/2005

  
HENRY W. H. TSAI  
PRIMARY EXAMINER

10/17/05